

Final Exam, 14 de enero de 2011, 6:45pm - 8:45pm, S-207
ICOM 4215: Computer Architecture and Organization, Sec 080
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Items

1. Design of a microprocessor
 - a. ALU Design
 - b. Controller Design
 - c. Fetch-Execute Cycle
2. Basic and fundamental concepts
 - a. AND, OR, XOR, NOR, NAND, NOT gates
 - b. Multiplexers, demultiplexers
 - c. Shift registers
 - d. Adders
 - e. Encoders, decoders
 - f. Flip Flops
 - g. Latches
 - h. Clock
 - i. Combinational and Sequential circuits
3. Selecting memory, accessing memory
4. Bus
5. SRC
- 6.