ICOM 4215 Fall 2011, Sections 040 and 050
December 14, 2011, 2:15pm to 4:15pm in Stefani 229
Final Exam
Open book, open notebook, 2 hours.

The following units have been studied:
- □ History of computers
- □ Instruction set architecture
- □ Addressing Modes
- □ SRC
- □ RTN
- □ Real machines: mobile, workstation, and hpc processors
- □ 1-bus microarchitecture and data path
- □ Register File
- □ Instruction Register
- □ Program Counter
- □ Arithmetic and Logic Unit
- □ Interface to Memory
- □ Data Path
- □ Controller
- □ Shift registers
- □ The design process
- □ Memory: cache

The exam will be a design exam. I will give you some specifications of a processor and you will design its components.

Open book exam. Please bring your book. You may also bring copies of the lecture slides.

Good luck.

Nayda Santiago