Run-time Environments

Lecture 8

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Status

- We have covered the front-end phases
 - Lexical analysis
 - Parsing
 - Semantic analysis
- Next are the back-end phases
 - Optimization
 - Code generation
- We'll do code generation first . . .

Run-time environments

- Before discussing code generation, we need to understand what we are trying to generate
- There are a number of standard techniques for structuring executable code that are widely used

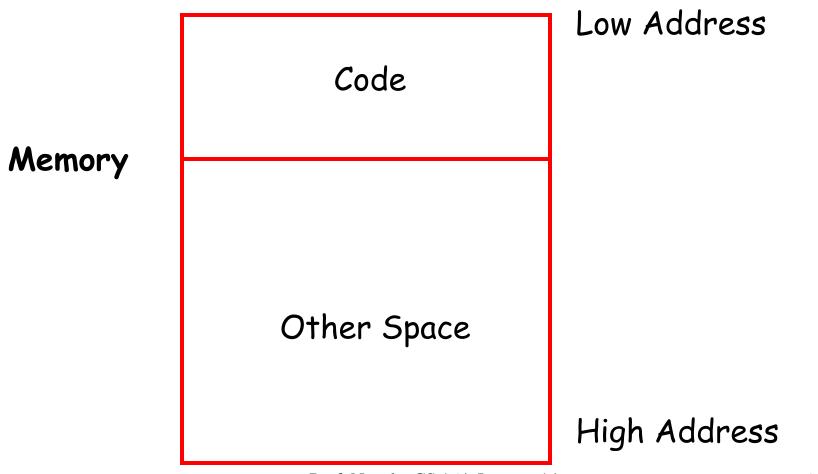
Outline

- Management of run-time resources
- Correspondence between static (compile-time) and dynamic (run-time) structures
- Storage organization

Run-time Resources

- Execution of a program is initially under the control of the operating system
- When a program is invoked:
 - The OS allocates space for the program
 - The code is loaded into part of the space
 - The OS jumps to the entry point (i.e., "main")

Memory Layout



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Notes

- Our pictures of machine organization have:
 - Low address at the top
 - High address at the bottom
 - Lines delimiting areas for different kinds of data
- These pictures are simplifications
 - E.g., not all memory need be contiguous
- In some textbooks lower addresses are at bottom

What is Other Space?

- Holds all data for the program
- Other Space = Data Space
- Compiler is responsible for:
 - Generating code
 - Orchestrating use of the data area

Code Generation Goals

- Two goals:
 - Correctness
 - Speed
- Most complications in code generation come from trying to be fast as well as correct

Assumptions about Execution

- 1. Execution is sequential; control moves from one point in a program to another in a welldefined order
- 2. When a procedure is called, control eventually returns to the point immediately after the call

Do these assumptions always hold?

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Activations

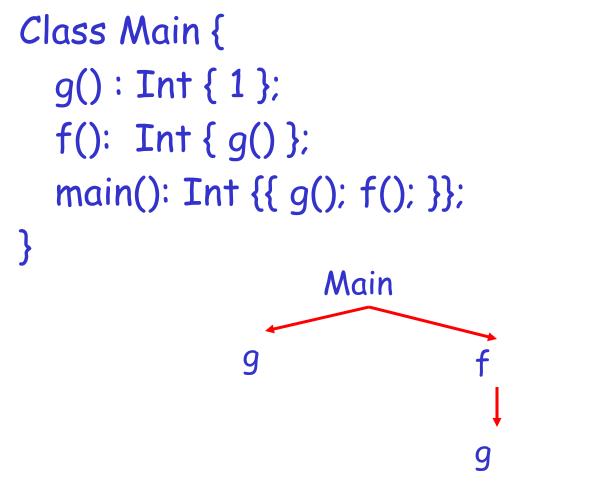
- An invocation of procedure P is an <u>activation</u> of P
- The lifetime of an activation of P is
 - All the steps to execute P
 - Including all the steps in procedures that P calls

Lifetimes of Variables

- The <u>lifetime</u> of a variable x is the portion of execution in which x is defined
- Note that
 - Lifetime is a dynamic (run-time) concept
 - Scope is a static concept

Activation Trees

- Assumption (2) requires that when P calls Q, then Q returns before P does
- Lifetimes of procedure activations are properly nested
- Activation lifetimes can be depicted as a tree



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```
Class Main {
g() : Int { 1 };
f(x:Int): Int { if x = 0 then g() else f(x - 1) fi};
main(): Int {{f(3); }};
}
```

What is the activation tree for this example?

```
Class Main {

g(): Int { 1 };

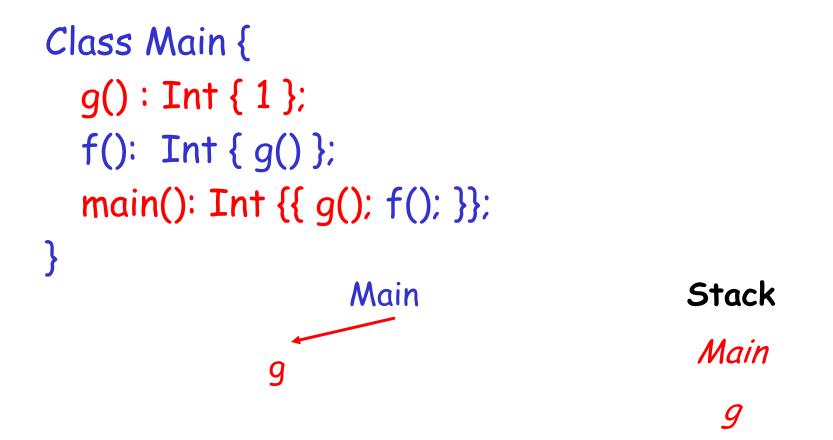
f(): Int { g() };

main(): Int {{ g(); f(); }};

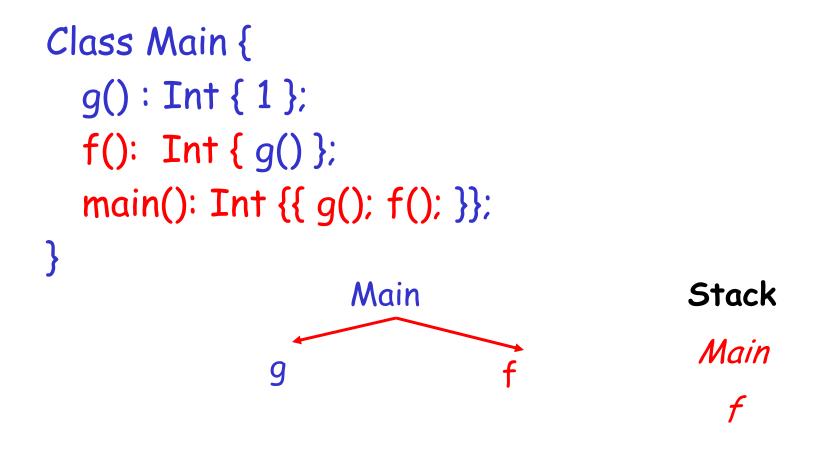
}

Main
```

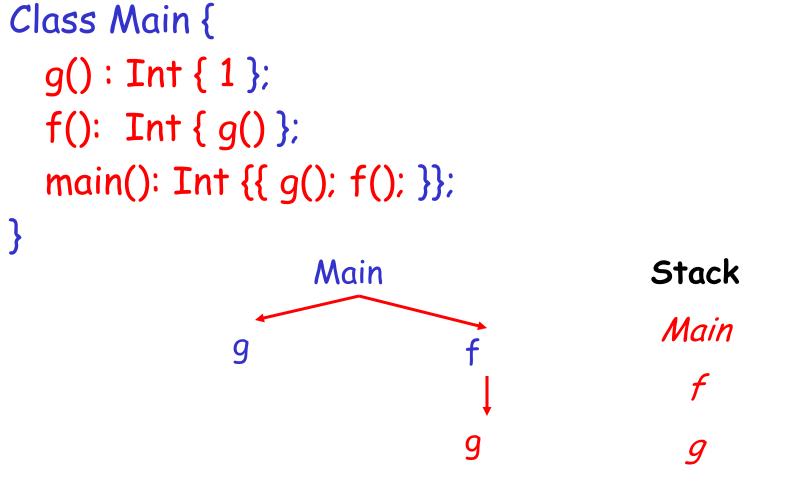
Stack *Main*



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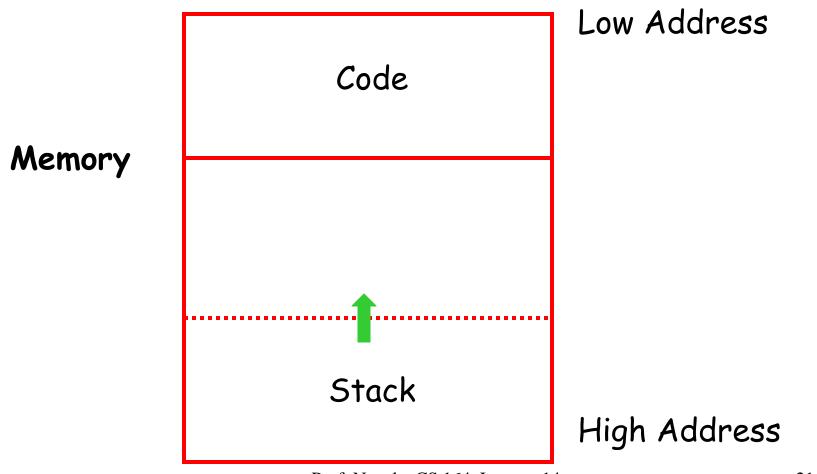


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Notes

- The activation tree depends on run-time behavior
- The activation tree may be different for every program input
- Since activations are properly nested, a stack can track currently active procedures

Revised Memory Layout



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Activation Records

- On many machines the stack starts at highaddresses and grows towards lower addresses
- The information needed to manage one procedure activation is called an <u>activation</u> <u>record</u> (AR) or <u>frame</u>
- If procedure F calls G, then G's activation record contains a mix of info about F and G.

What is in G's AR when F calls G?

- F is "suspended" until G completes, at which point F resumes. G's AR contains information needed to resume execution of F.
- G's AR may also contain:
 - Actual parameters to G (supplied by F)
 - G's return value (needed by F)
 - Space for G's local variables

The Contents of a Typical AR for G

- Space for G's return value
- Actual parameters
- Pointer to the previous activation record
 - The control link points to AR of caller of G
- Machine status prior to calling G
 - Contents of registers & program counter
 - Local variables
- Other temporary values

Example 2, Revisited

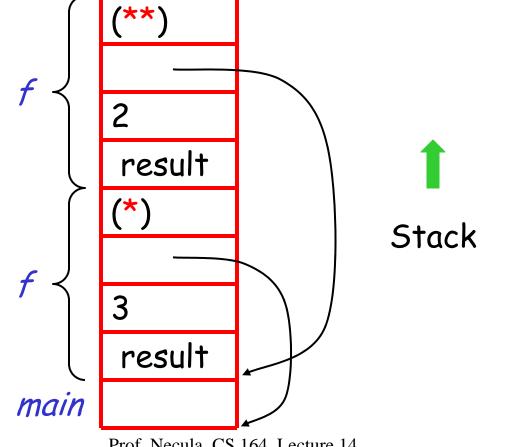
AR for f:

```
Class Main {
g() : Int { 1 };
f(x:Int):Int {if x=0 then g() else f(x - 1)(**)fi};
main(): Int {{f(3); (*) }};
```

return address
control link
argument
result

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Stack After Two Calls to f



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Notes

- main has no argument or local variables and its result is never used; its AR is uninteresting
- (*) and (**) are return addresses of the invocations of f
 - The return address is where execution resumes after a procedure call finishes
- This is only one of many possible AR designs
 - Would also work for C, Pascal, FORTRAN, etc.

The Main Point

The compiler must determine, at compile-time, the layout of activation records and generate code that correctly accesses locations in the activation record

Thus, the AR layout and the code generator must be designed together!

Discussion

- The advantage of placing the return value 1st in a frame is that the caller can find it at a fixed offset from its own frame
- There is nothing magic about this organization
 - Can rearrange order of frame elements
 - Can divide caller/callee responsibilities differently
 - An organization is better if it improves execution speed or simplifies code generation

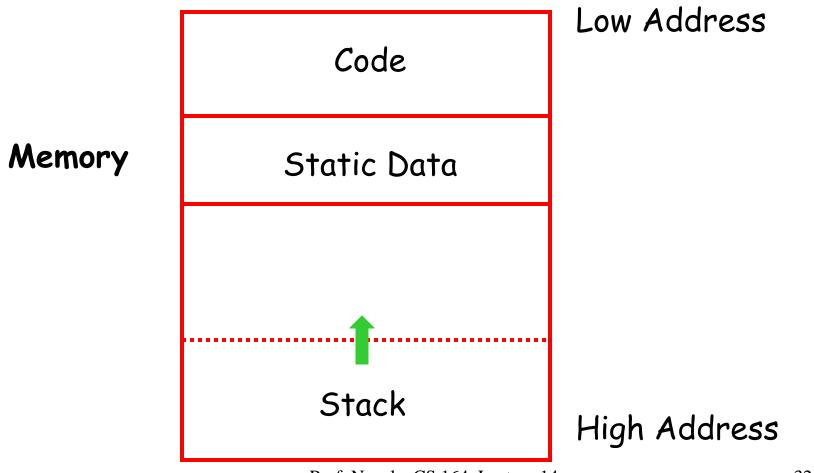
Discussion (Cont.)

- Real compilers hold as much of the frame as possible in registers
 - Especially the method result and arguments

Globals

- All references to a global variable point to the same object
 - Can't store a global in an activation record
- Globals are assigned a fixed address once
 - Variables with fixed address are "statically allocated"
- Depending on the language, there may be other statically allocated values

Memory Layout with Static Data



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Heap Storage

 A value that outlives the procedure that creates it cannot be kept in the AR method foo() { new Bar }

The Bar value must survive deallocation of foo's AR

 Languages with dynamically allocated data use a <u>heap</u> to store dynamic data

Notes

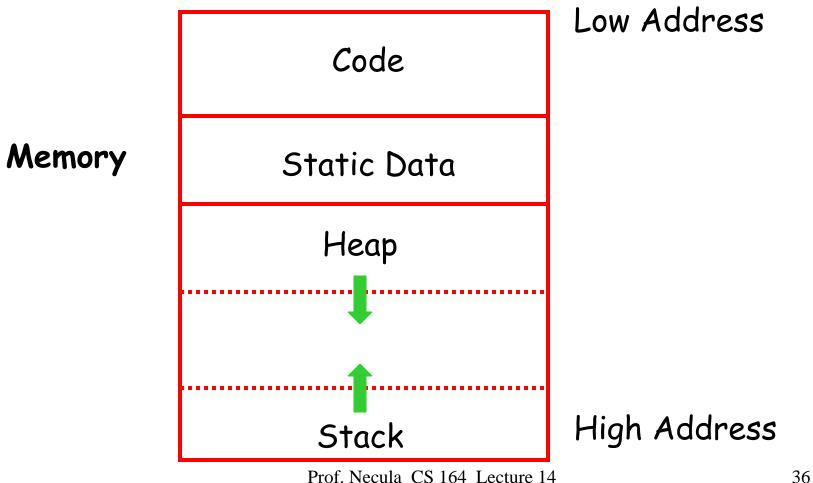
- The code area contains object code
 - For most languages, fixed size and read only
- The static area contains data (not code) with fixed addresses (e.g., global data)
 - Fixed size, may be readable or writable
- The stack contains an AR for each currently active procedure
 - Each AR usually fixed size, contains locals
- Heap contains all other data
 - In C, heap is managed by malloc and free

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Notes (Cont.)

- Both the heap and the stack grow
- Must take care that they don't grow into each other
- Solution: start heap and stack at opposite ends of memory and let the grow towards each other

Memory Layout with Heap



Data Layout

- Low-level details of machine architecture are important in laying out data for correct code and maximum performance
- Chief among these concerns is <u>alignment</u>

Alignment

- Most modern machines are (still) 32 bit
 - 8 bits in a byte
 - 4 bytes in a word
 - Machines are either byte or word addressable
- Data is word aligned if it begins at a word boundary
- Most machines have some alignment restrictions
 - Or performance penalties for poor alignment

Alignment (Cont.)

• Example: A string

"Hello"

Takes 5 characters (without a terminating 0)

- To word align next datum, add 3 "padding" characters to the string
- The padding is not part of the string, it's just unused memory