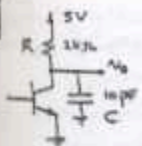


Probs. sugeridos: 16-21 y 23

- 14.16** Consider the circuit of Fig. 14.9 with a 5-V supply, $R = 4 \text{ k}\Omega$, $R_C = 2 \text{ k}\Omega$, $V_{BE} = 0.7 \text{ V}$, $V_{CEsat}(Q_3) \approx 0.2 \text{ V}$, $\beta_F = 20$, and $\beta_R = 0.1$. What input current flows with input high ($\geq 1.4 \text{ V}$)? with input low (at 0.2 V)? What is the value of V_{OH} with no load? For what fan-out of similar circuits does V_{OH} decrease by 2 V ?
- 14.17** Consider the circuit of Fig. 14.9 with $V_{CC} = 3 \text{ V}$, $R = 3 \text{ k}\Omega$, and $R_C = 1 \text{ k}\Omega$, as the input rises slowly from 0 V . If V_{CEsat} of Q_1 is 0.1 V , and if Q_3 turns on when its V_{BE} reaches 0.6 V , at what value of input voltage does Q_3 begin to conduct? This is a good estimate of V_{IL} .
- 14.18** A variant of the T²L gate shown in Fig. 14.19 is being considered in which all resistances are tripled. For input high, estimate all node voltages and branch currents with $\beta_F = 30$, $\beta_R = 0.01$, $V_{BE} = 0.7 \text{ V}$, and a load of $1 \text{ k}\Omega$ connected to the 5-V supply.
- 14.19** Repeat the analysis of the circuit suggested in Problem 14.18 with input low (at $+0.2 \text{ V}$) and a resistor of $1 \text{ k}\Omega$ connected from the output to ground.
- 14.20** Two TTL gates of the type described in Problem 14.18, one with input high and one with input low, have their outputs accidentally joined. What output voltage results? What current flows in the short circuit?
- 14.21** A transistor for which $\beta_F = 50$ and $\beta_R = 5$ is used for Q_3 in Fig. 14.20. For a base current of 2.5 mA , what is V_{CEsat} for $i_L = 0, 1, 10,$ and 100 mA ? Estimate R_{CEsat} at $0.5, 5,$ and 50 mA .
- 14.22** Consider the output circuit of the gate in Fig. 14.22. What is the output voltage when a current of 2 mA is extracted? What is the (small-signal) output resistance at this current level? Use $\beta = 50$.
- 14.23** Consider the output circuit of the gate in Fig. 14.22. For $\beta = \infty$ and $V_{CEsat} = 0.2 \text{ V}$, at what output current does Q_4 saturate? For $\beta = 20$, at what current does saturation occur?
- 14.24** If the output of the circuit in Fig. 14.22 is short-circuited to ground, what current flows? Assume high β , $V_{CEsat} = 0.2 \text{ V}$, and $V_{BE} = V_D = 0.7 \text{ V}$. What is the minimum value of β for which your analysis holds?

15  $\beta_c = 10, \beta_F = 50$
 $I_{C_{sat}} = \frac{5-0.2}{2k} = 2.4 \mu A$
 Base current = $2.4/10 = 0.24 \mu A$

For full time (on turn-on): $I_C = 50 \times 0.24 = 12 \mu A$
 $v_o = 5 - (2k)(12 \mu A)(1 - e^{-t/RC}) = -19 + 24 e^{-t/RC}$
 Now, $v_o = 0.2 + 0.1(5-0.2) = 0.68 V$, when
 $0.68 = -19 + 24 e^{-t/RC}$, or $e^{-t/RC} = \frac{19.32}{24} = 0.82$
 or $-t/RC = -0.198$, or $t = 0.198 RC$
 That is, $t_p = 0.198 \times 2 \times 10^3 \times 10 \times 10^{-12} = 3.97 \mu s$

For rise time (on turnoff):
 $v_o = 0.2 + (5-0.2)(1 - e^{-t/RC}) = 5 - 4.8 e^{-t/RC}$
 Now, v_o reaches $0.2 + 0.1(5-0.2) = 0.68 V$, when
 $0.68 = 5 - 4.8 e^{-t/RC}$, or $t = -70 \times 10^{-9} \ln \frac{5-0.68}{5-0.2} = 2.1 \mu s$
 and reaches $5 - 0.1(5-0.2) = 4.52$, when
 $4.52 = 5 - 4.8 e^{-t/RC}$, or $t = -70 \times 10^{-9} \ln \frac{5-4.52}{5-0.2} = 46.0 \mu s$
 That, $t_r = 46.0 - 2.1 = 43.9 \mu s$

16 For input high: $I_{B1} = \frac{5-0.7-0.7}{4k} = 0.9 \mu A$
 and $I_{in} = \beta_F I_{B1} = 0.1(0.9) = 90 \mu A$
 $V_{CE} \approx 0.2V$, but more precisely:
 $I_{B3} = 0.9 + 0.09 = 0.99 \mu A$; $I_{C3} = \frac{5-0.2}{2k} = 2.4 \mu A$
 $\therefore \beta_F = 2.4/0.99 = 2.42$
 Thus $V_{CE_{sat}} = V_T \ln \left(\frac{1 - (2.42 + 1)/0.01}{1 - 2.42/30} \right) = 923 mV$

14 For input low: $I_{B1} = I_{E1} = I_{B1} = \frac{5-0.7-0.2}{4k} = 1.025 \mu A$

V_{OH} with no load, is $5V$
 For a fanout of N , decrease in output is $N \times 20 \times 10^{-6} \times 2 \times 10^3 = 180 N \times 10^{-3}$
 For a 2V drop, $2 = 180 N \times 10^{-3} \rightarrow N = \frac{2000}{180} = 11.1$
 Thus a fanout of 11 causes about 2V output drop.

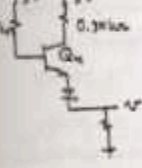
17 $V_{CE} \approx 0.6V - 0.1V = 0.5V$

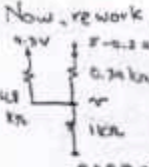
18 All resistors in Fig 14.19 are tripled.

For V_{CE} high:
 See $V_{B3} = 0.7V = V_{E2}$; $V_{B2} = 1.4V = V_{C1}$; $V_{B1} = 2.1V$.
 For V_{C2} : $V_{C2} = V_{E2} = 0.7V$
 $I_{C2} \approx \frac{5-0.7}{2(1.1)} = 0.897 \mu A$
 $I_{B2} \approx \frac{5-2.1}{2(4)} = 0.242 \mu A$
 $\therefore \beta_{F11} = \frac{0.897}{0.242} = 3.71$
 $V_{CE_{sat}} = 25 \ln \left(\frac{1 + (3.71 + 1)/0.01}{1 - 3.71/30} \right)$
 $= 25 \ln 579 = 157 mV$
 $\therefore V_{C2} = V_{E2} + 0.16 = 0.86V$
 $I_{C2} = \frac{5-0.86}{2(1.1)} = 0.83 \mu A$
 $V_{B4} = 0.86V$; $V_{E4} = 0.86 - 0.7 = 0.16$

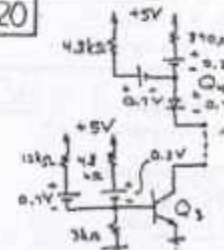
14 $I_{E2} = 0.83 + 0.242 = 1.075 \mu A$
 $I_{B3} = 1.075 - \frac{0.7}{3k} = 0.812 \mu A$
 Now, for $V_{CE} = 0.2V$, $I_{C3} = \frac{5-0.2}{12k} = 4.8 \mu A$
 $\beta_{F11} = \frac{4.8}{0.812} = 5.92$
 $\therefore V_{CE_{sat}} = 25 \ln \left(\frac{1 + (5.92 + 1)/0.01}{1 - 5.92/30} \right) = 167 mV$
 ie $V_{C3} = V_o = 0.17V$
 and $V_{C4} = 5V$, $I_{in} = 0.715 \times 0.01 = 7.25 \mu A$.

19 All resistors in Fig 14.19 tripled
 For V_{CE} low (0.2V):
 See $V_{E1} = 0.2V$, $V_{B1} = 0.9V$, $I_{B1} = \frac{5-0.9}{2k} = 0.242 \mu A$
 Also $I_{C1} = \beta_F I_{B1} = 0$, $V_{CE_{sat}} = 25 \ln \left(\frac{1 - (10 + 1)/0.01}{1 - 0.242/30} \right) = 15 mV$
 $V_{C1} = 0.2 + 0.15 = 0.35V = V_{B2}$
 and $V_{E2} = 0V$

 $\frac{5-0.4}{4k} = 1.15 \mu A$
 $V_o = \frac{1}{1.155} = 3.6 = 3.12V$
 $V_{E4} = 3.82V$
 $V_{B4} = 4.52V$
 $I_{E4} \approx \frac{3.12}{1k} = 3.12 \mu A$
 $V_{C4} = 5 - 0.312(3.12) = 3.78V$, Note saturated!
 (next)

Now, rework with $V_{CE4} = 0.2V$ (say), since Q_4 sat. **14**
 $\frac{4.3-0.2}{4.8} = \frac{V-0.7}{1}$
 $\approx 0.816 - 0.208V = 0.231 - 2.77 \mu A$
 ie $2.772 \mu A = 19.81$
 $V = \frac{13.31}{2.772} = 3.64V$

That is, $V_{E4} = 3.64V$, $V_{B4} = 3.64 + 0.2 = 3.84V$,
 $V_{C4} = 3.64 + 0.7 = 4.34V$, $V_o = 3.64 - 0.7 = 2.94V$,
 $I_{E4} = 2.94 \mu A / 1k = 2.94 \mu A$, $I_{E4} = \frac{5-2.94}{2k} = 2.55 \mu A$,
 $I_{B4} = \frac{5-4.34}{4.8k} = 0.13 \mu A$.
 Now, for Q_4 : $\beta_c = \frac{2.8}{0.13} = 21.52$
 $V_{CE_{sat}} = 25 \ln \left(\frac{1 + (21.52 + 1)/0.01}{1 - 21.52/30} \right) = 25 \ln \frac{703}{229} = 23.6 mV$
 which slightly exceeds the 20mV originally assumed.
 Thus V_o is lower by about $\frac{162}{2.94 + 0.02} = 54.4 \mu V$
 ie $V_o = 2.94 - 0.02 = 2.92V$

20  Assume that $V_{CE_{sat}} = 0.2V$
 In general, and that Q_4 saturates:
 $I_{B3} = \frac{5-1.4}{12} + \frac{5-0.9}{4.8} - \frac{0.7}{3}$
 $= 0.3 + 0.875 - 0.233$
 $= 0.921 \mu A$
 $I_{C3} \leq 30(0.921) = 27.6 \mu A$

$$I_{E4} = \frac{5 - 1.4 - V}{4.8} + \frac{5 - 0.7 - V}{0.21}$$

$$= 0.75 - 0.207V + 0.91 - 2.54V = 11.26 - 2.77V$$

Now, $27.6 > 11.26 - 2.77V$ for $V \approx 0$.
Thus Q_3 must also saturate, and $V_{CE} = V_{CE0}$
and the short-circuit current $= 11.26 - 2.77(0.2) = 10.7$
mA

More precisely - for $\beta = \frac{10.7}{0.921} = 11.62$

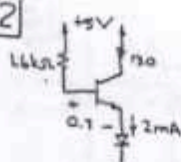
$$V_{CEsat} = V_T \ln \left(\frac{1 + (11.62 + 1)/0.2}{1 - 11.62/30} \right)$$

$$= 25 \ln \left(\frac{13.62}{-0.13} \right) = 191 \text{ mV}$$

$$\therefore V_0 = 0.19V$$

14

22



Assume linear operation:

$$V_B = 5 - 1.6 + \frac{2}{31} = 4.94V$$

$$V_C = 5 - 0.13 \times 2 = 4.74V$$

$$V_E = 4.94 - 0.7 = 4.24V$$

$$V_0 = 4.24 - 0.7 = 3.54V$$

Assume $\beta = 1$ for the diode (ie it is a diode-connected transistor), and $V_B = V_E = \frac{20 \text{ mV}}{2 - 1}$

$$\therefore R_{out} = \frac{25}{2} = \frac{25}{2} + \frac{1600}{31} = 12.5 + 51.6 = 64.1 \Omega$$

14

21 For $i_L = 0$, $\beta = 0$, and
 $V_{CEsat} = 25 \ln \left(\frac{1 + (0 + 1)/5}{1 - 0/50} \right) = 25 \ln 1.2 = 4.56 \text{ mV}$

For $i_L = 1 \text{ mA}$, $\beta = 1/2.5 = 0.4$, and
 $V_{CEsat} = 25 \ln \left(\frac{1 + (0.4 + 1)/5}{1 - 0.4/50} \right) = 25 \ln \frac{1.38}{0.92} = 6.37 \text{ mV}$

For $i_L = 10 \text{ mA}$, $\beta = 10/2.5 = 4$, and
 $V_{CEsat} = 25 \ln \left(\frac{1 + (4 + 1)/5}{1 - 4/50} \right) = 25 \ln \frac{3}{0.92} = 19.4 \text{ mV}$

For $i_L = 100 \text{ mA}$, $\beta = 100/2.5 = 40$, and
 $V_{CEsat} = 25 \ln \left(\frac{1 + (40 + 1)/5}{1 - 40/50} \right) = 25 \ln \frac{9.2}{0.2} = 98 \text{ mV}$

At 0.5 mA : $R_{CEsat} = \frac{6.37 - 4.56}{0.5} = 3.62 \Omega$

At 5.0 mA : $R_{CEsat} = \frac{19.4 - 4.56}{5} = 2.60 \Omega$

At 50 mA : $R_{CEsat} = \frac{98 - 4.56}{50} = 1.98 \Omega$

23 For $\beta = 0$, saturation occurs when the drop across 130Ω is $0.7 - 0.2 = 0.5V$, ie
for $I = 0.5/0.130 = 3.85 \text{ mA}$

For $\beta = 20$, and output I ,
 $I/21 = 1.6 + 0.7 - 0.2 = 20/21 I(0.13)$
 $1.6I = 10.5 = 2.6I$
 $I = 10.5 \text{ mA}$

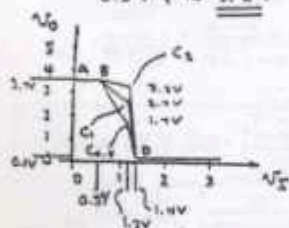
24 For high β , short-circuit current is
 $(5 - 0.7 - 0.7 - 0)/130 \Omega = 31.5 \text{ mA}$
Now, for the output at $0V$, $I_{B4} = \frac{5 - 0.7 - 0.7}{1.6 \text{ k}\Omega} = 2.25 \text{ mA}$
Thus β for which saturation occurs is $> \frac{31.5}{2.25} = 14$
That is, the minimum β for 31.5 mA is 14

25 The value of R_2 affects the output voltage for $V_E = 1.2V$ (point C of Fig 14.23).

For $R_2 = 1k\Omega$, $V_0 = 3.7V$

For $R_2 = 0.5k\Omega$, V_0 falls twice as much, ie by
 $2(3.7 - 2.7) = 2V$ to $3.7 - 2.7 = 1.0V$

For $R_2 = 2.0k\Omega$, V_0 falls by half as much, by
 $0.5V$, to $3.2V$



Apparently $R_2 = 2.0k\Omega$ gives the best transfer characteristic, being sharper (but is slow).

14

or $(1.976 - I/50 - 0.7)50 = I$
 $(0.876 - I/50)50 = I$, $43.8 - I = I$,
 $2I = 43.8$, $I = 21.9 \text{ mA}$

Check saturation of Q_4 :
 $V_{C4} = 5 - 0.13(21.9 - 0.7) = 2.31V$
while $V_{B4} = 2.55V$.

Thus $V_{C4} < V_{B4}$ shows Q_4 to be active!

Overall: $V_{C3} = 1.15V$, $V_{E1} = 1.30V$, $V_{B1} = 2.0V$, $V_{C1} = 1.4V$,
 $V_{E2} = 0.7V$, $V_{B4} = 1.85V$, $V_{E4} = 2.55V$, $V_{C4} = 2.31V$,
 $I_{E3} = 22.3 \text{ mA}$, $I_{E4} = 21.1 \text{ mA}$, $I_{E2} = 1.14 \text{ mA}$,
 $I_{E1} = 0.78 \text{ mA}$.

(b) For Q_2 , $r_{e2} = \frac{25}{1.14} = 21.9 \Omega$, $r_{\pi 2} = 1.12k\Omega$
 Q_3 , $r_{e3} = \frac{25}{22.3} = 1.12 \Omega$, $r_{\pi 3} = 57.3 \Omega$
 Q_4 , $r_{e4} = \frac{25}{21.1} = 1.18 \Omega$, $r_{\pi 4} = 60.4 \Omega$

For gain: There are two paths to the output, one via Q_3 and one via Q_4 , in which output currents, and gains, add. The highest-gain path (and the one most affecting the voltage gain) is via Q_2 and Q_4 .

Thus, for a 200Ω load (the input, to which the 200Ω feedback resistor is connected, being a zero-ohm source), working from the output back:

$$\text{Gain } B_4 \text{ to } V_0 = \frac{200}{200 + 2(1.18)} = 0.988 \text{ V/V}$$

$$R_{M4} = 51(200 + 2(1.18)) = 10.3 \text{ k}\Omega$$

$$\text{Gain } B_2 \text{ to } B_4 = \left(\frac{-20}{22.3} (10.3 \text{ k}\Omega) \right) / (21.9 + 1.18 \text{ k}\Omega)$$

Thus overall gain is about $-0.99(17.8) = -17.6 \text{ V/V}$

26 All but Q_1 are active with $V_{BE} = 0.7V$

(a) $V_E = 0.7 - 0.7 - 0.1 = 1.3V$
Correspondingly, $I_{E1} = (5 - 0.7 - 1.3)/4 = 0.75 \text{ mA}$
Drop in 200Ω resistor is $(0.200)(0.75) = 0.15V$
 $\therefore V_0 = 1.3 - 0.15 = 1.15V$

Correspondingly, $V_{B4} = 1.15V + 0.7 + 0.7 = 2.55V$
and $I_{B4} = (5 - 2.55)/1.14 = 1.93 \text{ mA}$

Apparently Q_4 and Q_3 will be conducting a similar current, I , in the collector of Q_3 , and

$I - 0.75$ in the emitter of Q_4 :
ie $(1.53 - \frac{I - 0.75}{\beta}) \frac{\beta}{20} = \frac{0.7}{1} I$