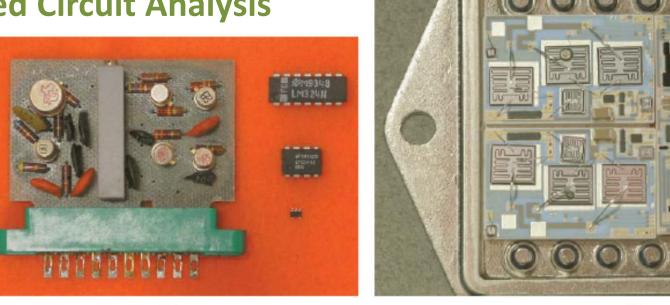
Last Lecture → Maximum Power Transfer

Determine the value of R₁ such that maximum power is Vout transferred from the source to the load. Max. Power Transfer Parameters (V, A, W) P_{out} 3 2 Current $P_{\rm out}/P_{\rm in}$ 2 8 10 $P_L = \frac{V_L^2}{R_I} = R_L \left[\frac{v}{(R_I + R)} \right]^2$ R_2/R_1 $\frac{\delta P_L}{\delta R_L} = \left[\frac{\nu}{(R_L + R)}\right]^2 - 2 \cdot R_L \left[\frac{\nu}{(R_L + R)}\right]^2 \frac{1}{R_L + R} \implies \frac{\delta P_L}{\delta R_L} = 0 \quad \rightarrow R_L = R \qquad \eta = \frac{P_L}{P_{source}} = \frac{R_L}{R_L + R}$

Operational Amplifiers → Chapter #4

- Amplifier Model
- Amplifier Based Circuit Analysis



Op-Amp

• Is the single most important integrated circuit for analog circuit design

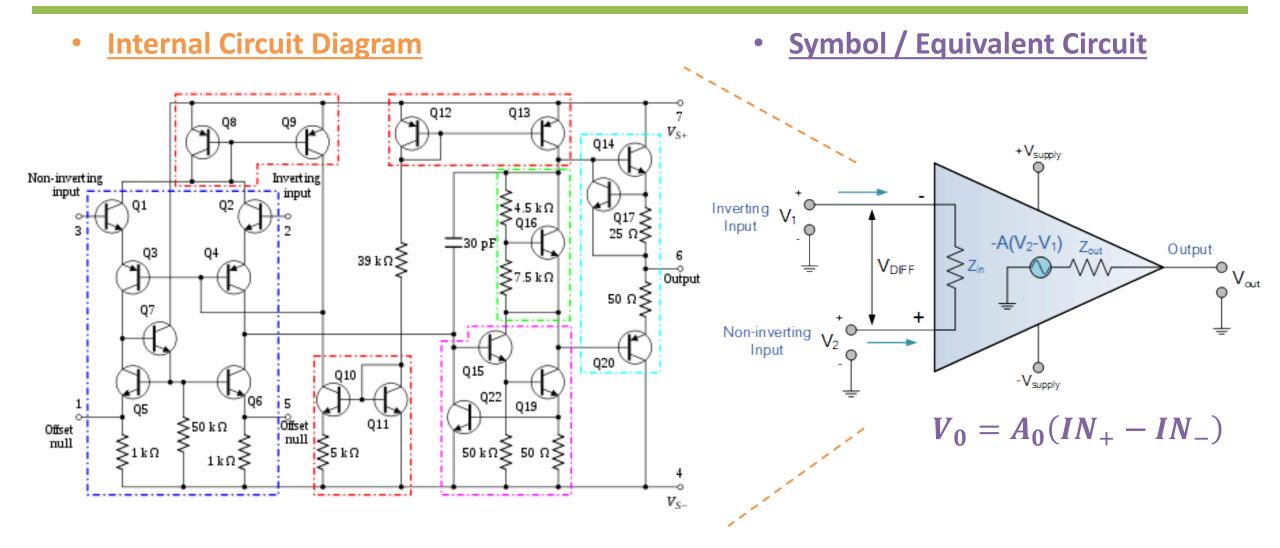
(a)

- Is a versatile interconnection of transistors and resistors
- Is used in a wide range of applications, from engine control systems to cellular phones
- Was designed to perform mathematical operations

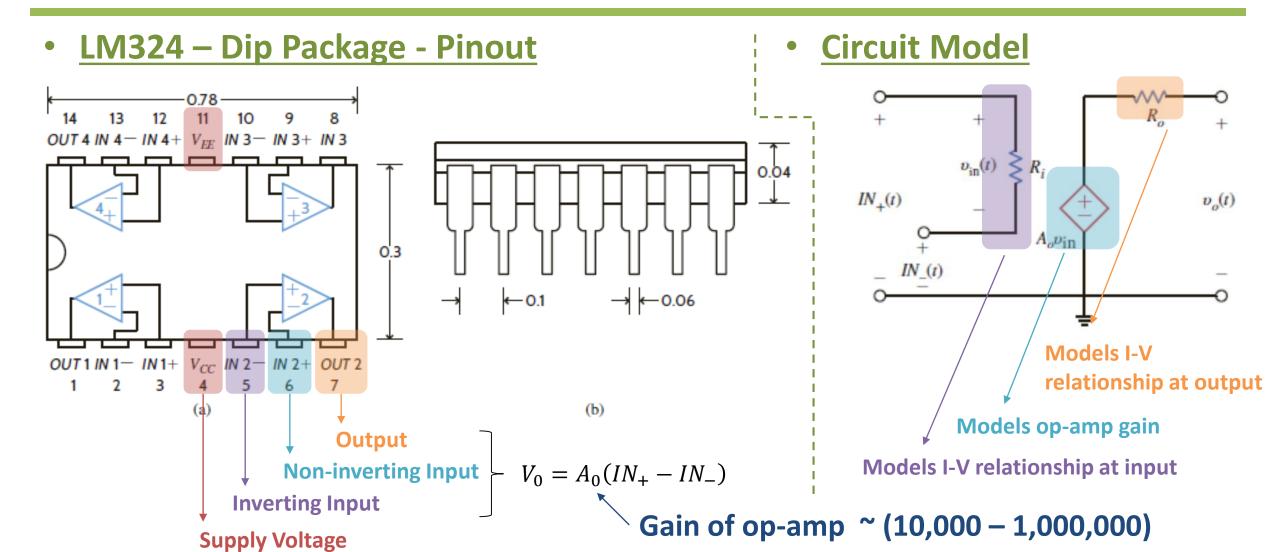
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Op-amps

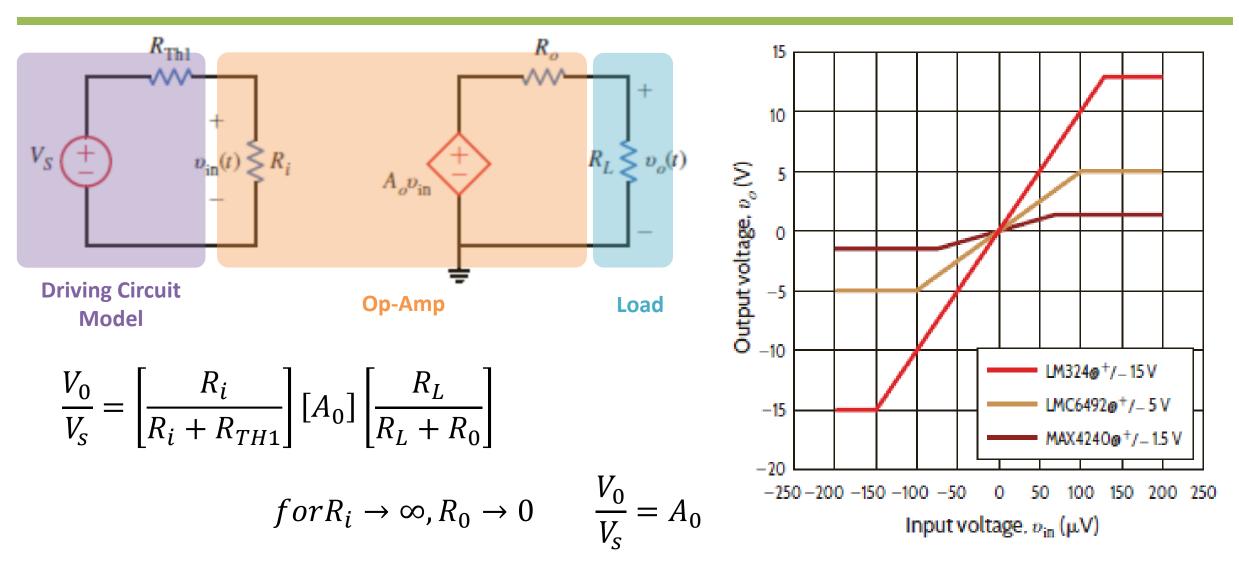
Operational Amplifier



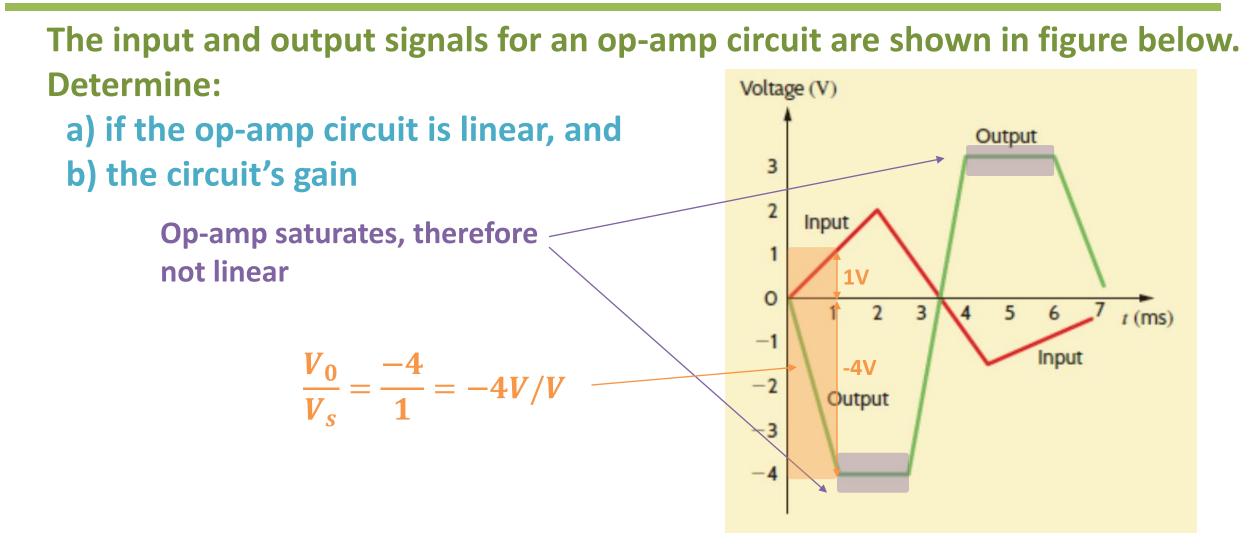
Op-Amp Model



Op-Amp Circuit



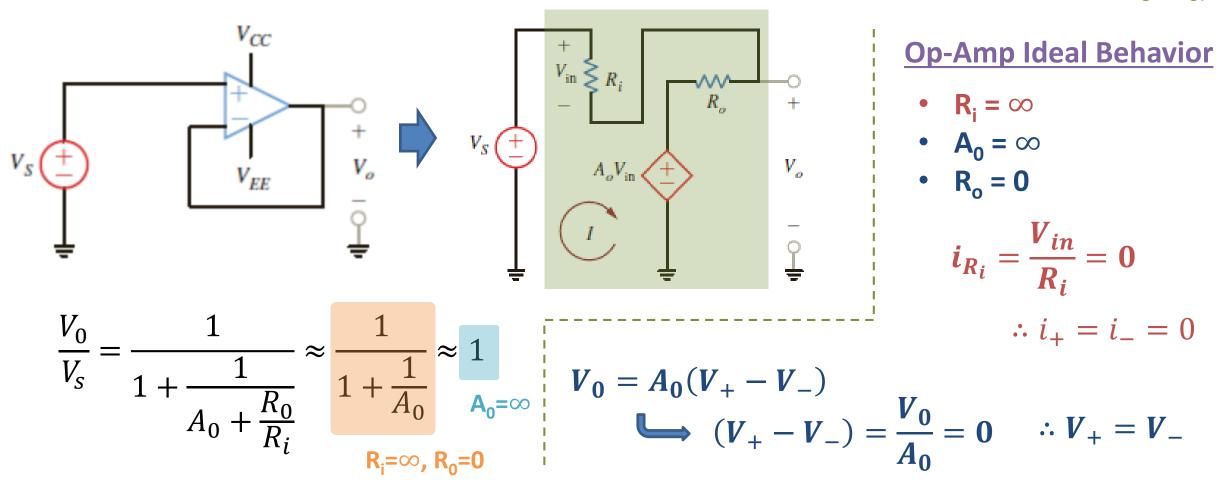
Example 4.1



Unity Gain Buffer

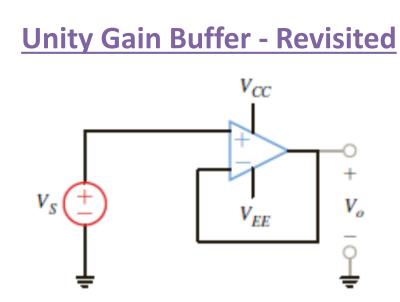
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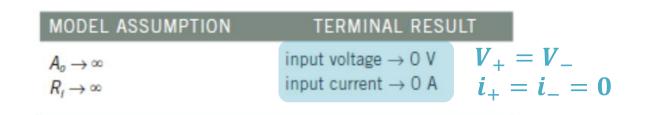
Using the op-amp model find the expression for the transfer function $V_o/V_{s.}$

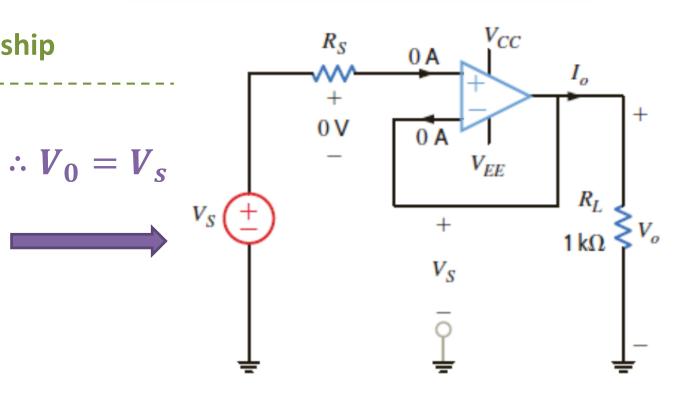


Ideal Op-Amp Circuit Analysis

- Stablish ideal op-amp conditions on the circuit schematic
- Write nodal equations at the op-amp input terminals
- Solve for the input/output relationship



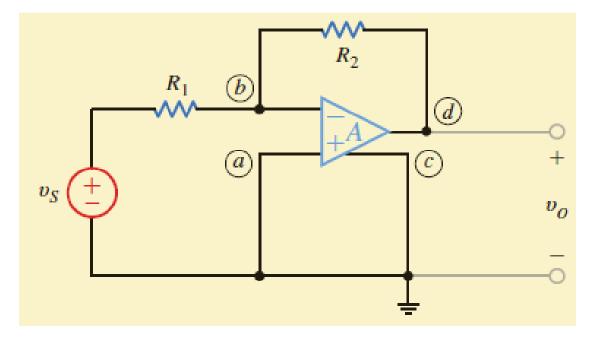




Example 4.2

10/2/2019

Determine the gain v_0/v_s of the basic inverting op-amp configuration using both the non-ideal ($R_i = \infty$, $R_o = 0$) and the ideal models.



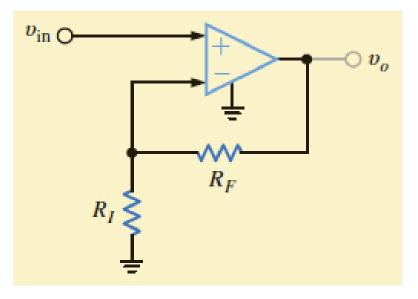
$$\frac{V_0}{V_s} = -\frac{R_2}{R_1} \left[\frac{1}{1 + \frac{1}{A_0} \left(1 + \frac{R_2}{R_1} \right)} \right]$$

$$for A_0 = \infty \rightarrow \frac{V_0}{V_s} \approx -\frac{R_2}{R_1}$$

Example 4.3

10/2/2019

Determine the gain v_0/v_s of the basic non-inverting op-amp configuration both the non-ideal ($R_i = \infty$, $R_o = 0$) and the ideal models. Determine the expression for the gain error (GE).



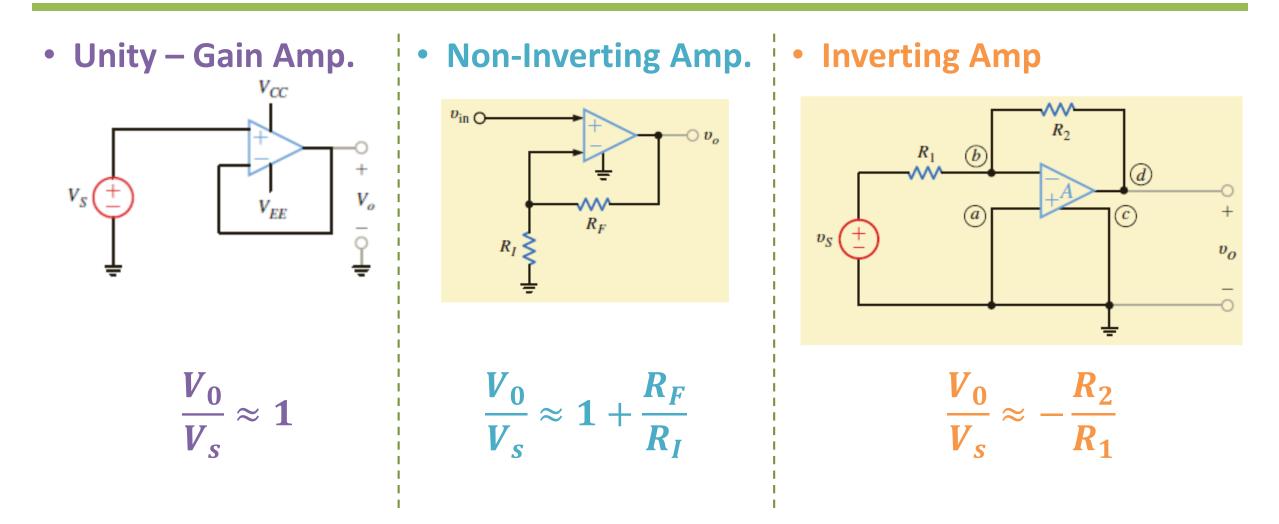
$$\frac{V_0}{V_s} = \left[1 + \frac{R_F}{R_1}\right] \left[\frac{1}{1 + \frac{1}{A_0}\left(1 + \frac{R_F}{R_1}\right)}\right]$$

$$for \ A_0 = \infty \ \rightarrow \frac{V_0}{V_s} \approx 1 + \frac{R_F}{R_1}$$

$$GE = \left[\frac{A_{v_{ideal}} - A_v}{A_{v_{ideal}}}\right] \cdot 100 = \left[\frac{1}{1 + A_0\left(\frac{R_1}{R_1 + R_F}\right)}\right] \cdot 100$$

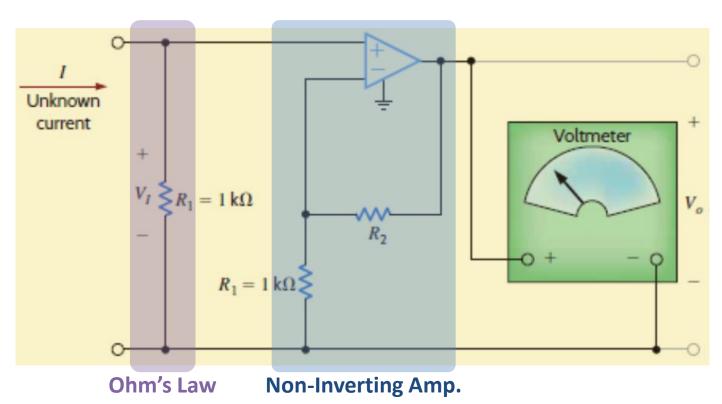
for $A_0 = \infty \rightarrow GE \approx 0$

Op-Amp Basic Circuits



Example 4.7

The provided circuits is an electronic ammeter. It operates as follows: the unknown current, I, through R_1 produces a voltage, V_1 . V_1 is amplified by the op-amp to produce a voltage, V_0 , which is proportional to I. The output voltage is measure with a simple voltmeter. Find the value of R_2 such that 10V appears at V_0 for each milliamp of unknown current.



$$\frac{V_0}{I} = \frac{10}{1m} = 10k\Omega$$

$$V_0 = \left[1 + \frac{R_2}{1k}\right]V_I$$

$$V_I = [1k] \cdot I$$

$$\therefore V_0 = [1k + R_2] \cdot I$$

$$\bigcup \frac{V_0}{I} = [1k + R_2] = 10k\Omega$$

$$\therefore R_2 = 9k\Omega$$

Problem

10/2/2019

Determine v_0 in the circuit provided.

